

PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet **1** of **3****Complete if Known**

Application Number	09/930,804
Filing Date	08/15/2001
First Named Inventor	Haywood
Art Unit	2665
Examiner Name	Nguyen
Attorney Docket Number	1000-P02154US

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US- 2002/0099855-A1	07-25-2002	Bass	
		US- 6,480,120	10-01-2002	Bass	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ³
		Country Code ⁴ Number ⁴ Kind Code ⁵ (if known)				

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	2	of	3
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Complete if Known

Application Number	09/930,804
Filing Date	08/15/2001
First Named Inventor	Haywood
Art Unit	2685
Examiner Name	Nguyen
Attorney Docket Number	1000-P02154US

U. S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet

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of

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Complete if Known

Application Number	09/930,804
Filing Date	08/15/2001
First Named Inventor	Haywood
Art Unit	2665
Examiner Name	Nguyen
Attorney Docket Number	I000-P02154US

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		PETASWITCH SOLUTIONS, INC., The Pisces Chipset, Product Brief, 2001, http://www.peta-switch.com/products/product_brief.htm .	
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		GUPTA, Scheduling in Input Queued Switches: A Survey, June 1996, Department of Computer Science, Stanford University, California.	
		SCHOENEN, ET AL., Distributed Cell Scheduling Algorithms for Virtual-Output-Queued Switches, December 1999, pp. 1211-1215, Volume 1, GLOBECOM, IEEE Global Telecommunications Conference.	
		FAHMY, A Survey of ATM Switching Techniques, August 14, 2001, Department of Computer and Information Science, The Ohio State University.	
		STILIADIS, ET AL., Rate-Proportional Servers: A Design Methodology for Fair Queueing Algorithms, December 1995, Computer Engineering & Information Sciences, University of California, Santa Cruz.	

Examiner Signature		Date Considered	
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